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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO. CONFIRMATION NO.	
09/895,198	07/02/2001	K. Paul Muller	YO999-247DIV	1509
<sup>29154</sup> FREDERICK V	7590 02/04/200 V. GIBB, III	EXAMINER		
Gibb Intellectua	al Property Law Firm, 1	TRINH, HOA B		
2568-A RIVA ROAD SUITE 304			ART UNIT	PAPER NUMBER
ANNAPOLIS,	MD 21401	2893		
		MAIL DATE	DELIVERY MODE	
		02/04/2009	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary		Applica	tion No.	Applicant(s)					
		09/895,	198	MULLER ET AL.					
		Examine	ər	Art Unit					
		НОА В.	TRINH	2893					
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).									
Status									
1) 又	Responsive to communication(s) filed	on <i>02 Januarv</i> 20	09.						
·	This action is <b>FINAL</b> . 2b) ☐ This action is non-final.								
	/ <del></del>								
•	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.								
Dispositi	on of Claims								
4)🛛	☑ Claim(s) <u>30-44</u> is/are pending in the application.								
	4a) Of the above claim(s) is/are withdrawn from consideration.								
	5) Claim(s) is/are allowed.								
6)🖂	6)⊠ Claim(s) <u>30-44</u> is/are rejected.								
7)									
8)□	Claim(s) are subject to restriction	on and/or election	requirement.						
Application Papers									
9)□ -	Γhe specification is objected to by the Ι	Examiner.							
10) 🔲 -	Γhe drawing(s) filed on is/are: a	ı)∏ accepted or t	o) objected to by the	Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).									
	Replacement drawing sheet(s) including th	e correction is requ	ired if the drawing(s) is ob	jected to. See 37 C	FR 1.121(d).				
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.									
Priority u	nder 35 U.S.C. § 119								
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>									
Attachment			4)	((DTO 442)					
2) D Notice 3) D Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTC nation Disclosure Statement(s) (PTO/SB/08) No(s)/Mail Date	D-948)	4)  Interview Summary Paper No(s)/Mail D 5)  Notice of Informal D 6)  Other:	ate					

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### **DETAILED ACTION**

### Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 01/02/2009 has been entered.

# Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:
  - 1. Determining the scope and contents of the prior art.
  - 2. Ascertaining the differences between the prior art and the claims at issue.
  - 3. Resolving the level of ordinary skill in the pertinent art.
  - 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out

the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

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5. Claim 30-44 are rejected under 35 U.S.C. 103(a) as being unpatentable over Rostoker (5,662,768; reference of record cited on 04/12/2002).

**As to claim 30**, Rostoker discloses an IC structure comprising a substrate 4 (fig. 1f) having an upper surface, wherein said substrate 4 comprises a substrate material 4; an opening (trench, fig. 1f) in the substrate 4 defined by said substrate material 4, wherein borders of the substrate material form a first rectangular portion originating with an intersection of said upper surface and said opening and a second rectangular portion, wherein the first rectangular portion has a larger dimensions in a horizontal direction than the second and third rectangular portions, wherein said horizontal direction is between sidewalls of said first rectangular portion and said second rectangular portion, and wherein said sidewalls are perpendicular to said upper surface; and a conductor 32 (figs. 1f, 3) filling the opening (trench, figs. 1f, 3). See marked-up figure 1f below.

With respect to the comparative rectangular portions and their respective dimensions of the first, second and third rectangular portions, it is well known in the semiconductor art to size and configure different structures in the device. Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the invention of Rostoker with the dimensions of the first, second, and third portions, as claimed, because it has been held that where the general conditions of the claims are disclosed in the prior art, a change in thickness, size, or shape without a

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significant function is not inventive to discover the optimum or workable range by routine experimentation. See <u>In re Aller</u>, 220 F.2d 454, 105 USPQ 233,235 (CCPA 1955).

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As to claims 31, 41, the second rectangular is deeper in the opening (trench; fig. 1f) than the first rectangular portion (fig. 1f). See marked-up figure 1f below.

**As to claims 32, 42,** the first rectangular portion is deeper than the second rectangular portion. See marked-up figure 1f below.

**As to claims 33, 43,** the second rectangular portion increases a surface area of said opening (trench). See marked-up figure 1f below.

**As to claims 34, 44**, the second rectangular portion inherently increases a capacitance of the structure. See marked-up figure 1f below.

As to claim 35, Rostoker discloses an IC structure comprising a substrate 4 (fig. 1f) having an upper surface, wherein said substrate 4 comprises a substrate material 4; an opening (trench, fig. 1f) in the substrate 4 defined by said substrate material 4, wherein borders of the substrate material form a first rectangular portion originating with an intersection of said upper surface and said opening, a second rectangular portion and a third rectangular portion, wherein the first rectangular portion has a larger dimensions in a horizontal direction than the second and third rectangular portions, wherein said horizontal direction is between sidewalls of said first rectangular portion, second and said third rectangular portion, and wherein said sidewalls are perpendicular to said upper surface; and a conductor 32 (figs. 1f, 3) filling the opening (trench, figs. 1f, 3). See marked-up figure 1f below.

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With respect to the comparative rectangular portions and their respective dimensions of the first, second and third rectangular portions, it is well known in the semiconductor art to size and configure different structures in the device. Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the invention of Rostoker with the dimensions of the first, second, and third portions, as claimed, because it has been held that where the general conditions of the claims are disclosed in the prior art, a change in thickness, size, or shape without a significant function is not inventive to discover the optimum or workable range by routine experimentation. See <u>In re Aller</u>, 220 F.2d 454, 105 USPQ 233,235 (CCPA 1955).

**As to claim 36**, said second rectangular portion is between said first rectangular portion and said third rectangular portion. See marked-up figure 1f below.

**As to claim 37**, said first rectangular portion and said third rectangular portion have substantially similar dimensions. See marked-up figure 1f below.

**As to claim 38**, said second rectangular portion increases increase a surface area of said structure. See marked-up figure 1f below.

As to claim 39, the first rectangular portion and the third rectangular portion have different dimensions (fig. 1f) in said horizontal direction. See marked-up figure 1f below.

As to claim 40, Rostoker discloses an IC structure comprising a substrate 4 (fig. 1f) having an upper surface, wherein said substrate 4 comprises a substrate material 4; a bottled shaped opening (trench, fig. 1f) in the substrate 4 defined by said substrate

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material 4, wherein borders of the substrate material form a first rectangular portion located completely below and having no portion thereof coincident with said upper surface, and a second rectangular portion located below said first rectangular portion and bounded on two sides by a mask material deposited on said substrate material, wherein the first rectangular portion has a larger dimensions in a horizontal direction than the second and third rectangular portions, wherein said horizontal direction is between sidewalls of said first rectangular portion and said second rectangular portion, and wherein said sidewalls are perpendicular to said upper surface; and a conductor 32 (figs. 1f, 3) filling the opening (trench, figs. 1f, 3). See marked-up figure 1f below.

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With respect to the comparative rectangular portions and their respective dimensions of the first, second and third rectangular portions, it is well known in the semiconductor art to size and configure different structures in the device. Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the invention of Rostoker with the dimensions of the first, second, and third portions, as claimed, because it has been held that where the general conditions of the claims are disclosed in the prior art, a change in thickness, size, or shape without a significant function is not inventive to discover the optimum or workable range by routine experimentation. See In re Aller, 220 F.2d 454, 105 USPQ 233,235 (CCPA 1955).

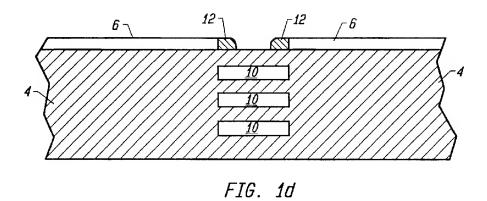
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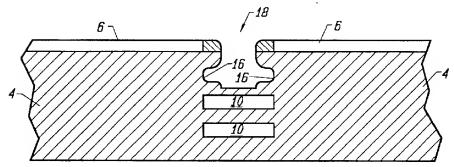
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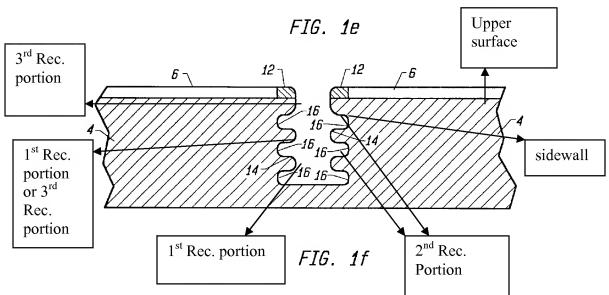
Sep. 2, 1997

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## Response to Arguments

1. Applicant's arguments with respect to the newly amended pending claims 30-44 have been considered but they are not persuasive.

- a. Objection of the specification is most in view of the newly amended claim 30.
- b. Rejection of claims 30-44: In the remarks, applicants contend that Rostoker does not disclose all of the limitation as claimed. However, the examiner disagrees with applicant's allegation of Rostoker. As stated in the above rejection, Rostoker discloses all of the limitations in claims. Rostoker discloses an IC structure comprising a substrate 4 (fig. 1f) having an upper surface, wherein said substrate 4 comprises a substrate material 4; an opening (trench, fig. 1f) in the substrate 4 defined by said substrate material 4, wherein borders of the substrate material form a first rectangular portion originating with an intersection of said upper surface and said opening and a second rectangular portion, wherein the first rectangular portion has a larger dimensions in a horizontal direction than the second and third rectangular portions, wherein said horizontal direction is between sidewalls of said first rectangular portion and said second rectangular portion, and wherein said sidewalls are perpendicular to said upper surface; and a conductor 32 (figs. 1f, 3) filling the opening (trench, figs. 1f, 3). Further, it is noted that regarding to the rectangular portions and the dimensions, it has been held that where the general conditions of the claims are disclosed in the prior art, a change in thickness, size, or shape without a significant function is not inventive to discover the optimum or workable range by routine experimentation. See In re Aller, 220 F.2d 454, 105 USPQ 233,235 (CCPA 1955).

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c. Therefore, it is still proper to apply Rostoker to the newly amended claims 30-40.

### Conclusion

- d. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to (Vikki) Hoa B. Trinh whose telephone number is (571) 272-1719. The Examiner can normally be reached from Monday-Friday, 9:00 AM 5:30 PM Eastern Time. If attempts to reach the examiner by telephone are unsuccessful, the Examiner's supervisor, Ms. Davienne Monbleau, can be reached at (571) 272-1945. The office fax number is 571-273-8300.
- e. Any request for information regarding to the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Also, status information for published applications may be obtained from either Private PAIR or Public Pair. In addition, status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <a href="http://pair-direct.uspto.gov">http://pair-direct.uspto.gov</a>. If you have questions pertaining to the Private PAIR system, please contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).
- f. Lastly, paper copies of cited U.S. patents and U.S. patent application publications have ceased to be mailed to applicants with Office actions since June 2004. Paper copies of foreign patents and non-patent literature will continue to be included with office actions. These cited U.S. patents and patent application publications are available for download via the Office's PAIR. As an alternate source, all U.S. patents and patent application publications are available on the USPTO web site (<a href="www.uspto.gov">www.uspto.gov</a>), from the Office of Public Records and from commercial sources. Applicants are referred to the

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Electronic Business Center (EBC) at <a href="http://www.uspto.gov/ebc/index.html">http://www.uspto.gov/ebc/index.html</a> or 1-866-217-9197 for information on this policy.

Requests to restart a period for response due to a missing U.S. patent or patent application publications will not be granted.

/(Vikki) Hoa B Trinh/

Examiner, Art Unit 2893